

# 7/B / 1/BIDWAY 5/6/03 Patent

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In Re Application of:   |                    |           |              |
|---|--------------------|-----------|--------------|
| Brian S. Doyle et al.   |                    |           |              |
| U.S. Serial No:   | 10/045,376         | Examiner: | Le, Dung Anh |
| Filed:  | November 9, 2001 ) | Art Unit: | 2818         |
| For: MOS TRANSISTOR USING ( MECHANICAL STRESS TO CONTROL) SHORT CHANNEL EFFECTS |                    |           |              |

Assistant Commissioner for Patents and Trademarks
Washington, D.C. 20231

## **AMENDMENT AND RESPONSE**

Dear Sir:

This is in response to the Office Action mailed April 1, 2003. Applicant has provided herewith a clean version of all claims 1-18 pending in the above referenced case. Additionally, provided herewith is a version of amended claims 1-18 showing changes made of claims.

| FIRST CLASS CERTIFICATE OF MAILING   | : 곱  |
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|  | : 끊  |
| I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner of Patents, Washington, D.C. 20231 | - CE |
| on April 21, 2003  | ~    |
| Date of Deposit  | 3 [  |
| Teresa Edwards O   | 3 0  |
| Name of Person Mailing Correspondence  |      |
| Signature April 21, 283  |      |